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 $\succ$

A diagram of a 10-bit shift register. It consists of a horizontal row of 10 rectangular cells. Above each cell is an input label: A, B, C, D, E, F, G, H, I, J. Below each cell is an output label: A, B, C, D, E, F, G, H, I, J. The cells are connected sequentially from left to right, with the output of one cell being the input to the next.

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中華民國 108 年 6 月 29 日 第 10 次 RM010-2
2019-06-29

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個	7739	1292	8339
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個	2012	1959	8667
個	8532	2548	8019
個	6375	8578	2359
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